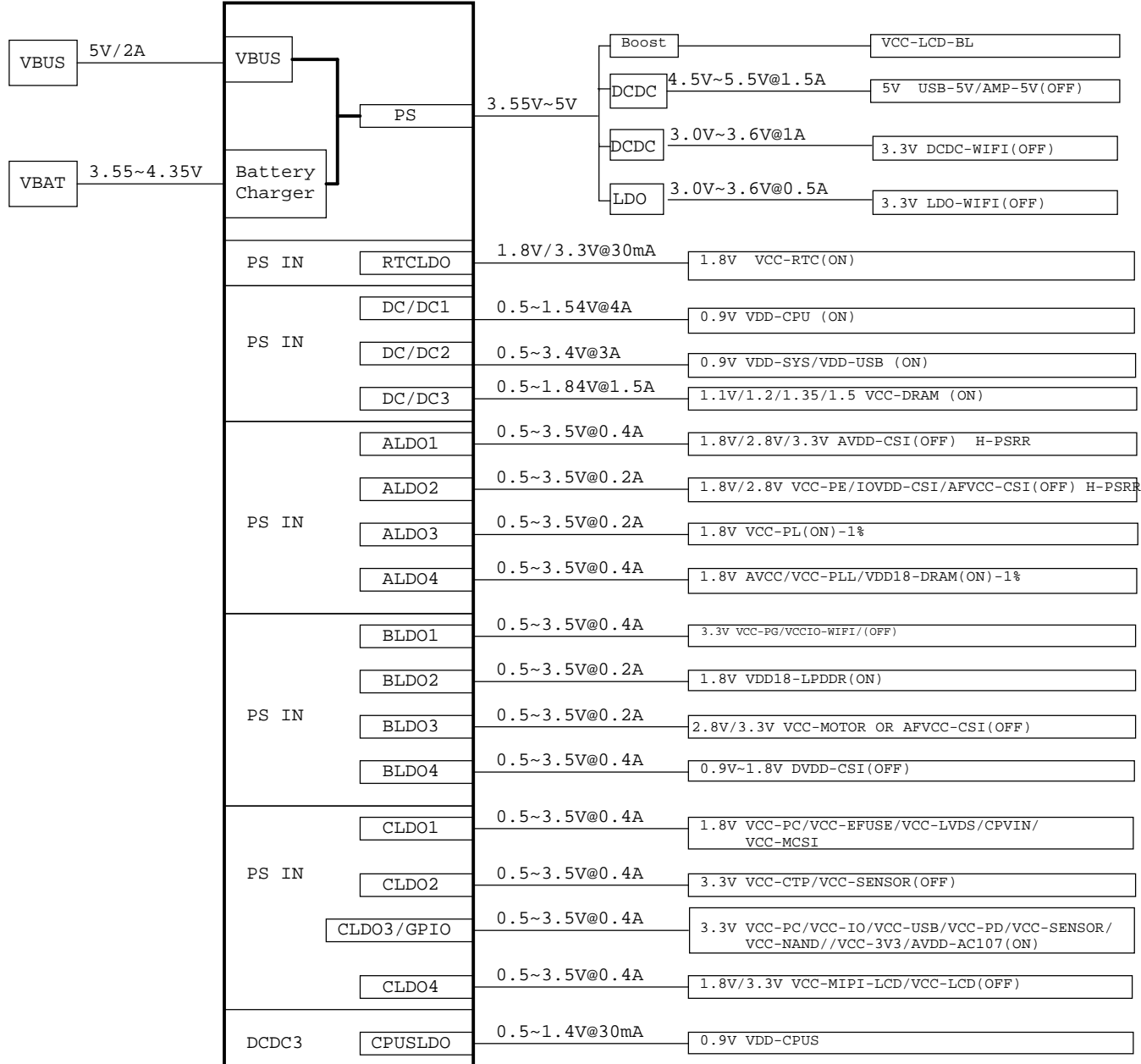


DEFAULT POWER ON

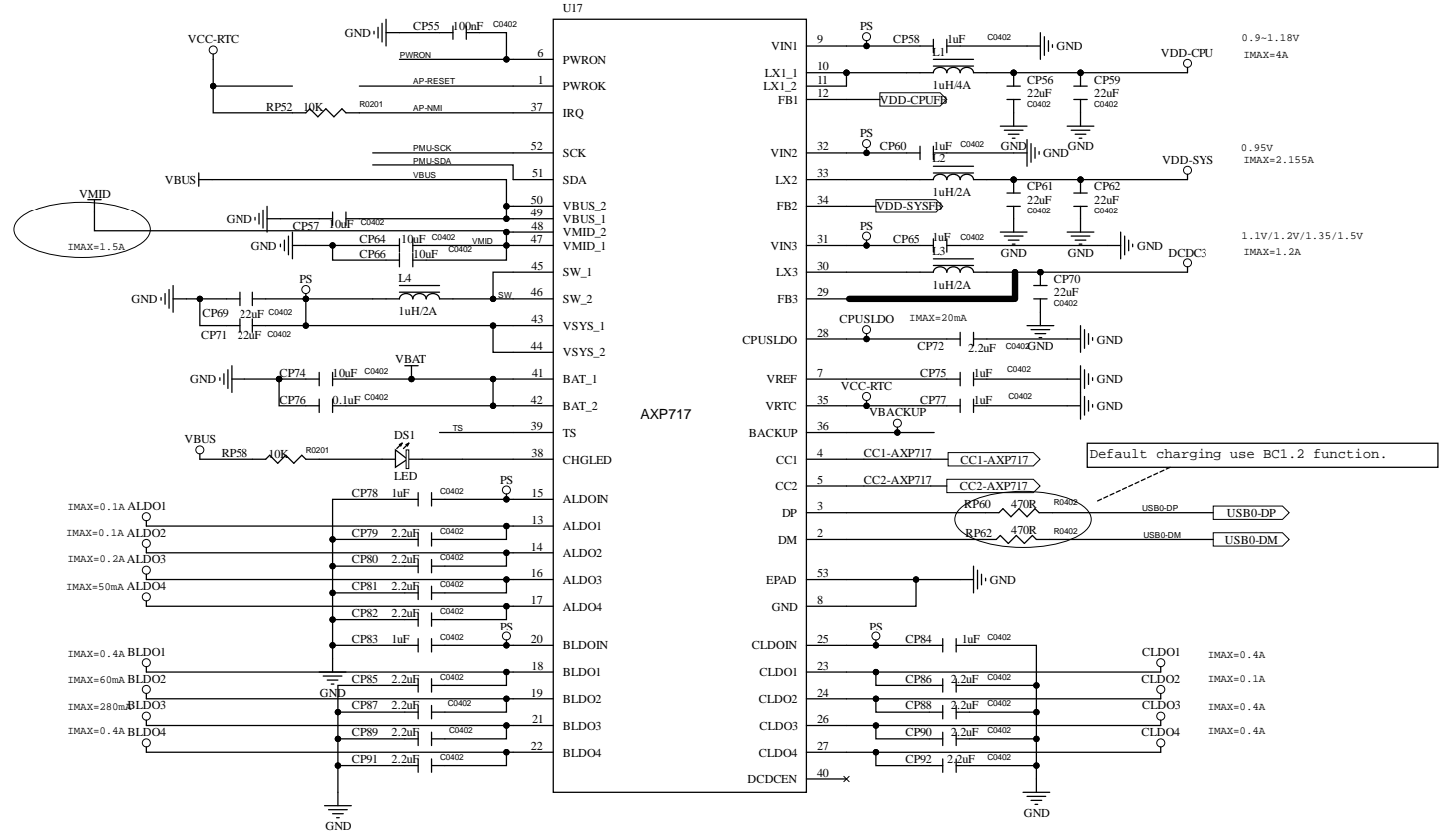
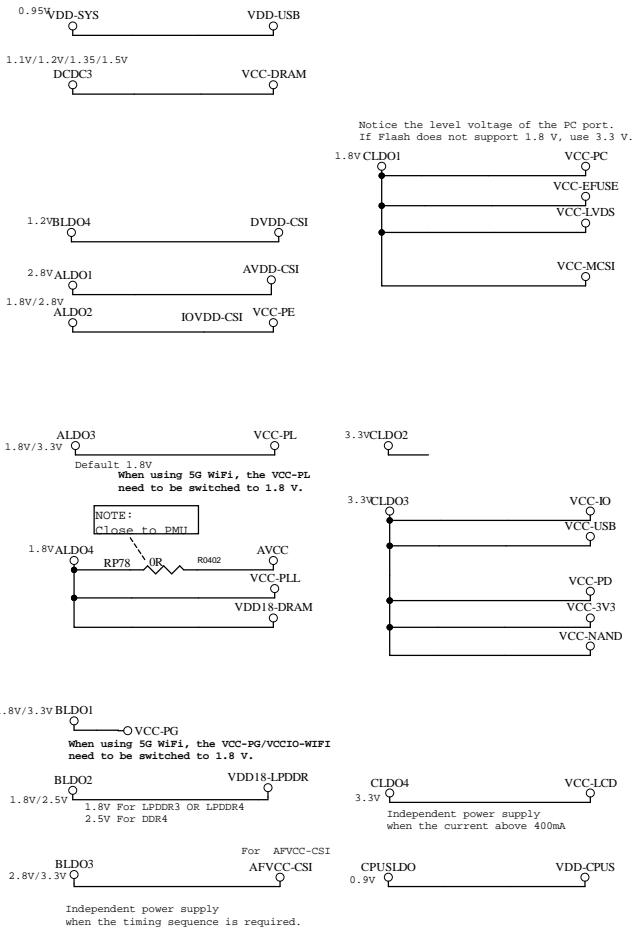
DEFAULT POWER OFF

AXP717



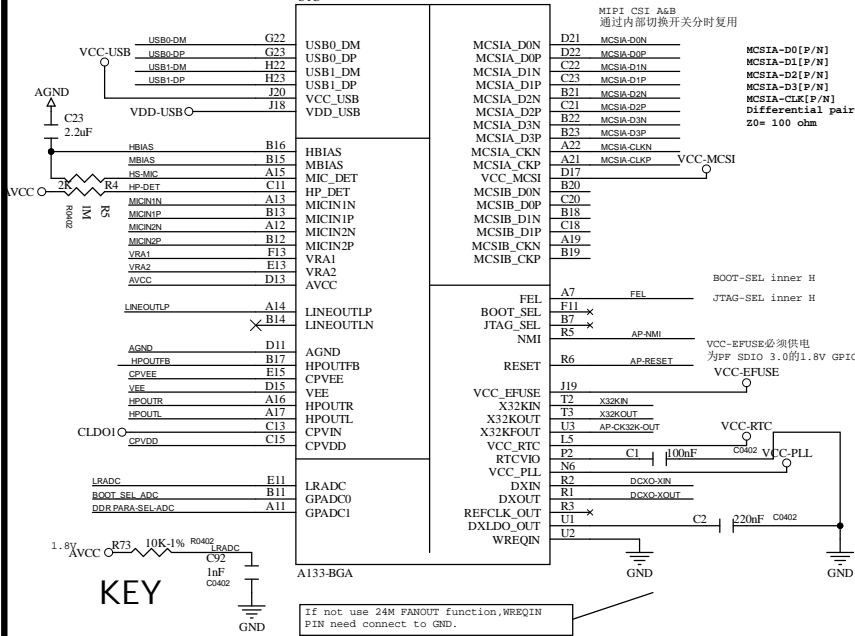
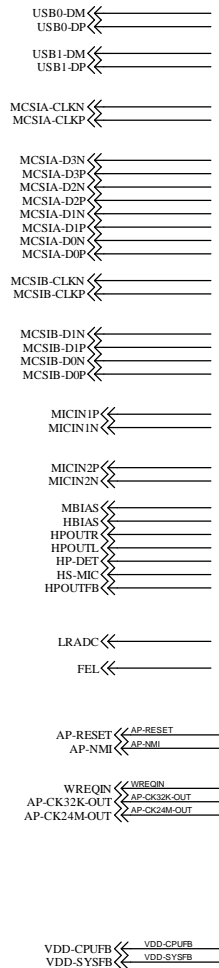
PMIC

AP-NMI	AP-NMI
PMU-SCK	PMU-SCK
PMU-SDA	PMU-SDA
AP-RESET	AP-RESET
PWRON	PWRON

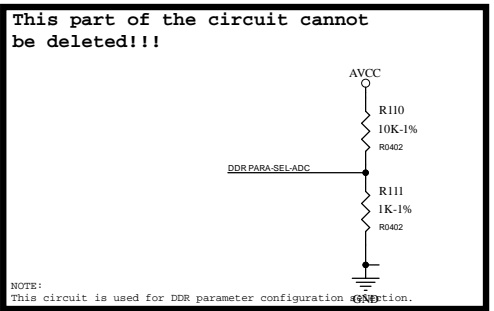


CPU SYS

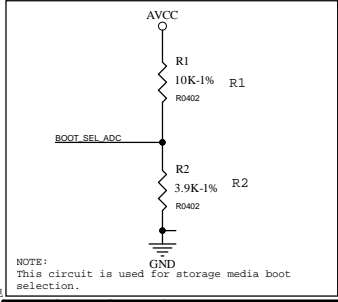
UIC



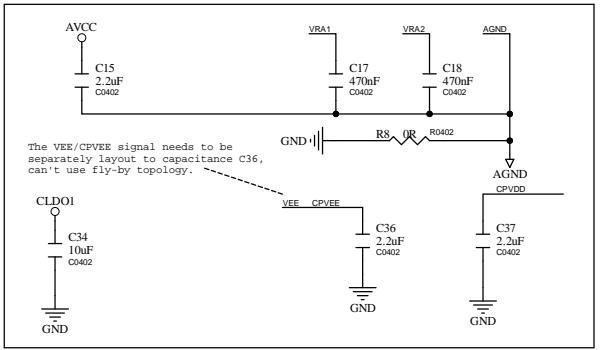
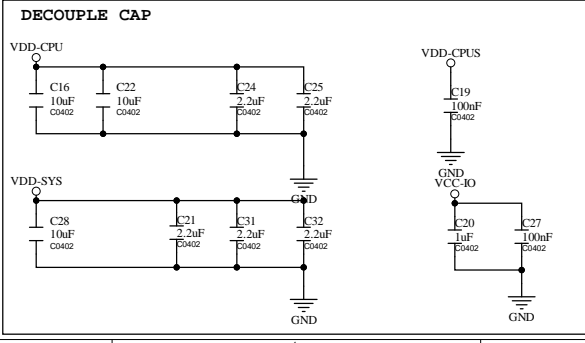
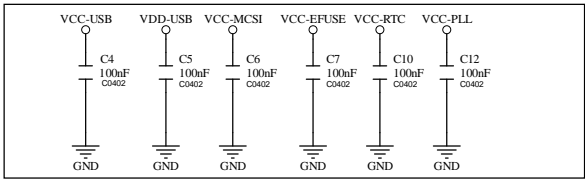
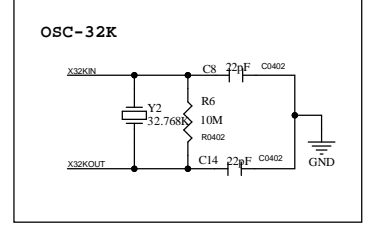
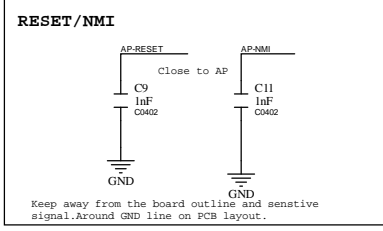
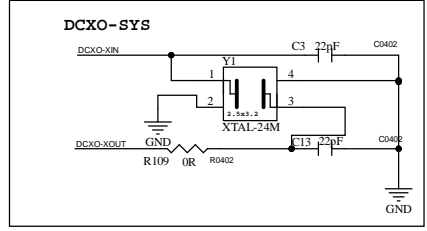
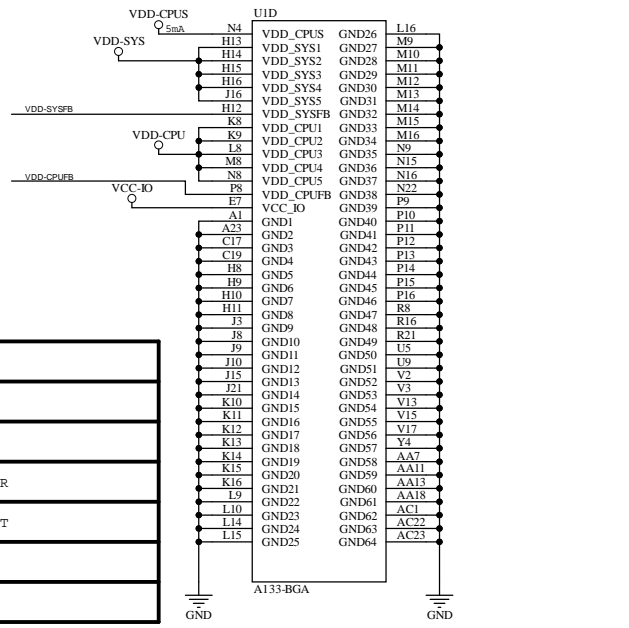
KEY

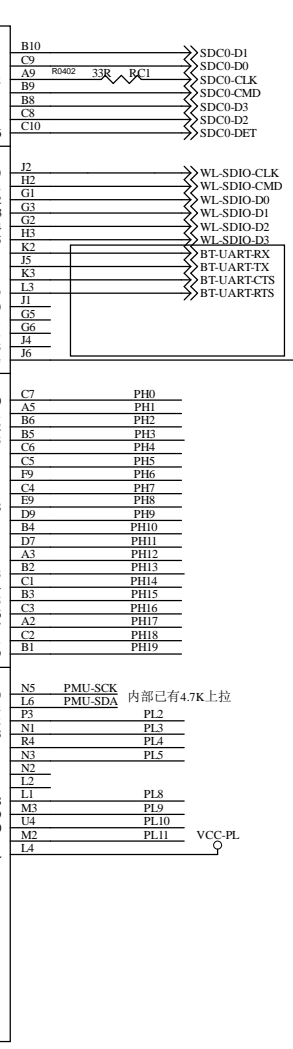
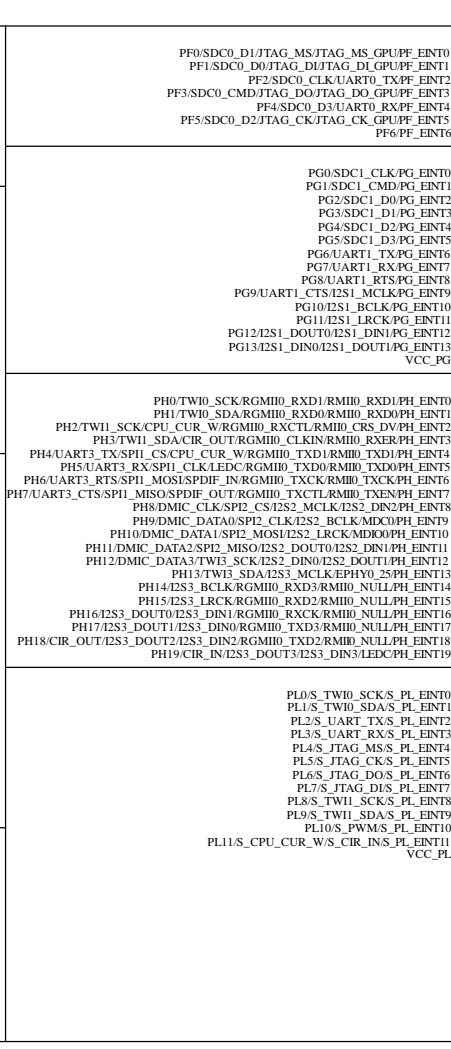
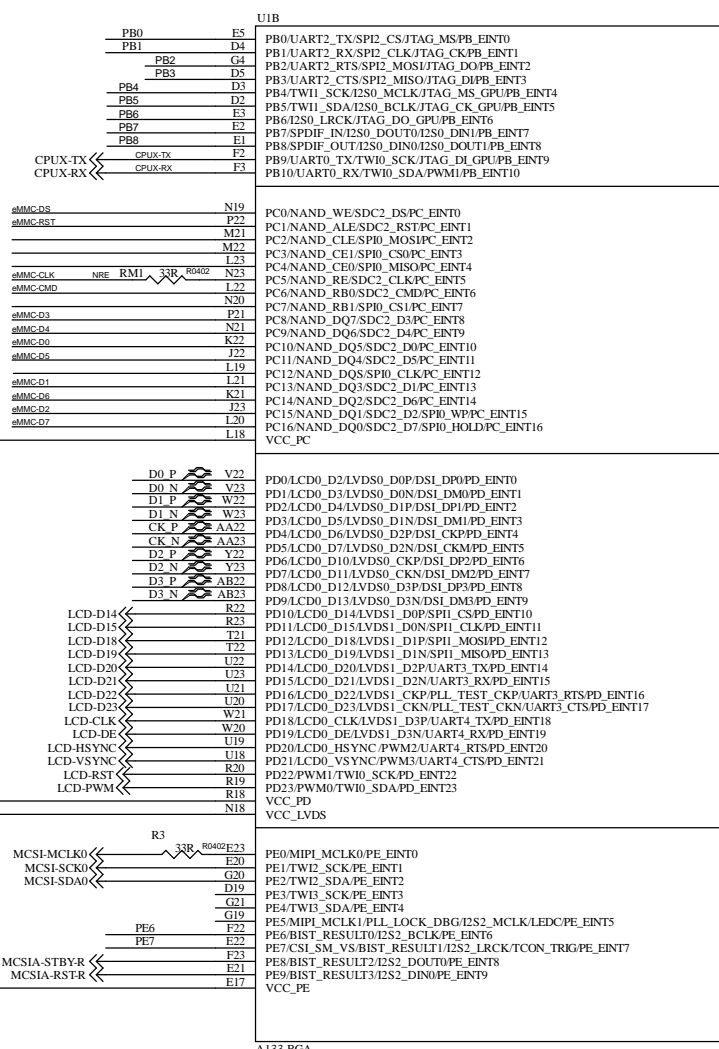


GPIO Level(Set by the R112 pull-up and R113 pull-down resistance of PH17 GPIO)	GPADC Voltage(Fixed pull-up R110 is 10K-1%,Set the voltage by adjusting pull-down resistor R111)	DDR PARA
0	163mV(1K-1%)	DDR PARA 1
0	382mV(2.7K-1%)	DDR PARA 2
0	608mV(5.1K-1%)	DDR PARA 3
0	811mV(8.2K-1%)	DDR PARA 4
0	1050mV(14K-1%)	DDR PARA 5
0	1315mV(27K-1%)	DDR PARA 6
0	1569mV(68K-1%)	DDR PARA 7
0	1800mV(NC)	DDR PARA 8
1	163mV(1K-1%)	DDR PARA 9
1	382mV(2.7K-1%)	DDR PARA 10
1	608mV(5.1K-1%)	DDR PARA 11
1	811mV(8.2K-1%)	DDR PARA 12
1	1050mV(14K-1%)	DDR PARA 13
1	1315mV(27K-1%)	DDR PARA 14
1	1569mV(68K-1%)	DDR PARA 15
1	1800mV(NC)	DDR PARA 16



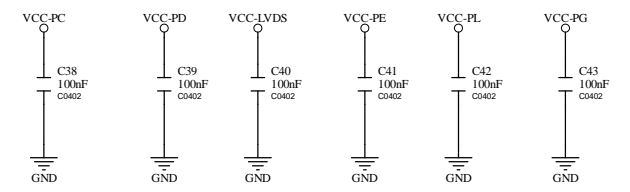
NO.	R1	R2	Boot Select type
1	NC	10K	SMHC0->MLC NAND->SLC NAND
2	10K	1K	SMHC0->SLC NAND->MLC NAND
3	10K	2K2	SMHC0->EMMC_BOOT->EMMC_USER
4	10K	3K9	SMHC0->EMMC_USER->EMMC_BOOT
5	6K8	4K7	SMHC0->SPI NOR
6	6K8	6K8	SMHC0->SPI NAND



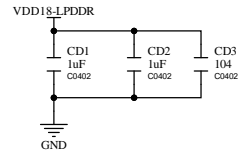
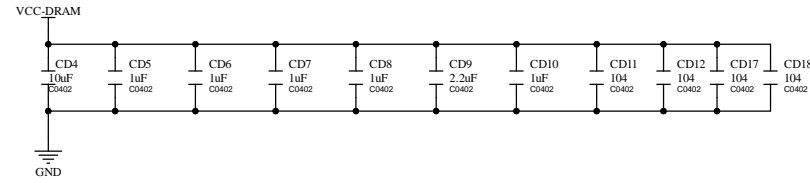
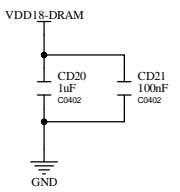
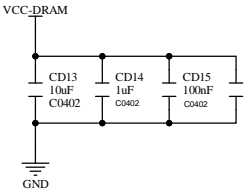
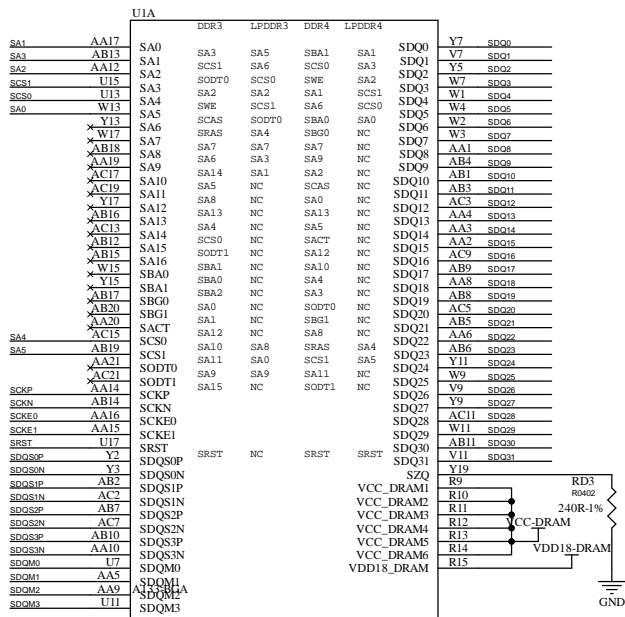


NOTE:
Serial port data signal needs to be reversed.

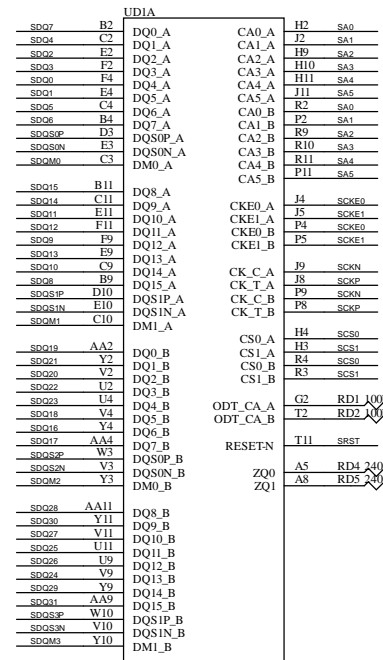
GPIO use guide:
1. Note that the voltage of SOC GPIO must matches the external IO voltage.
2. The pull up voltage of the GPIO is selected to correspond to the power field voltage of GPIO.



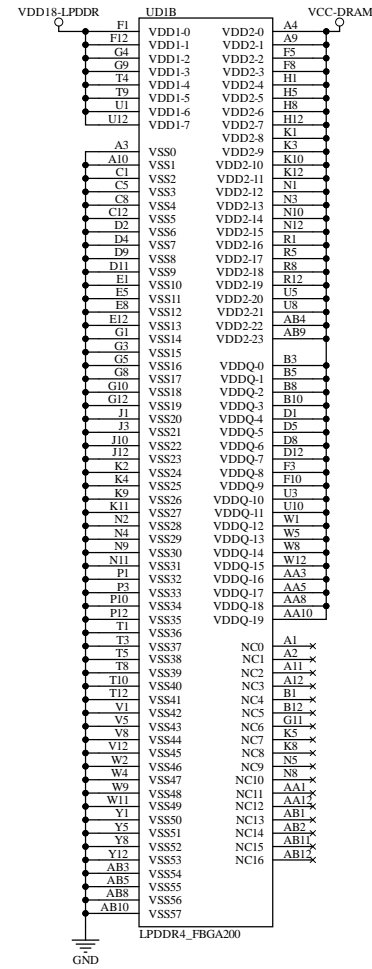
LPDDR4



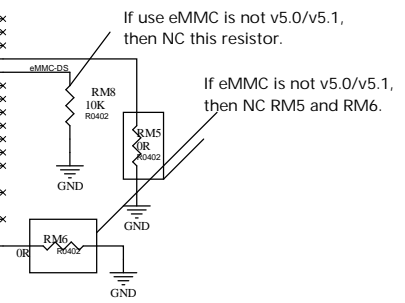
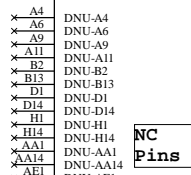
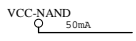
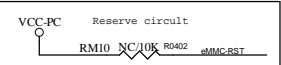
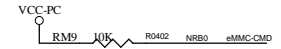
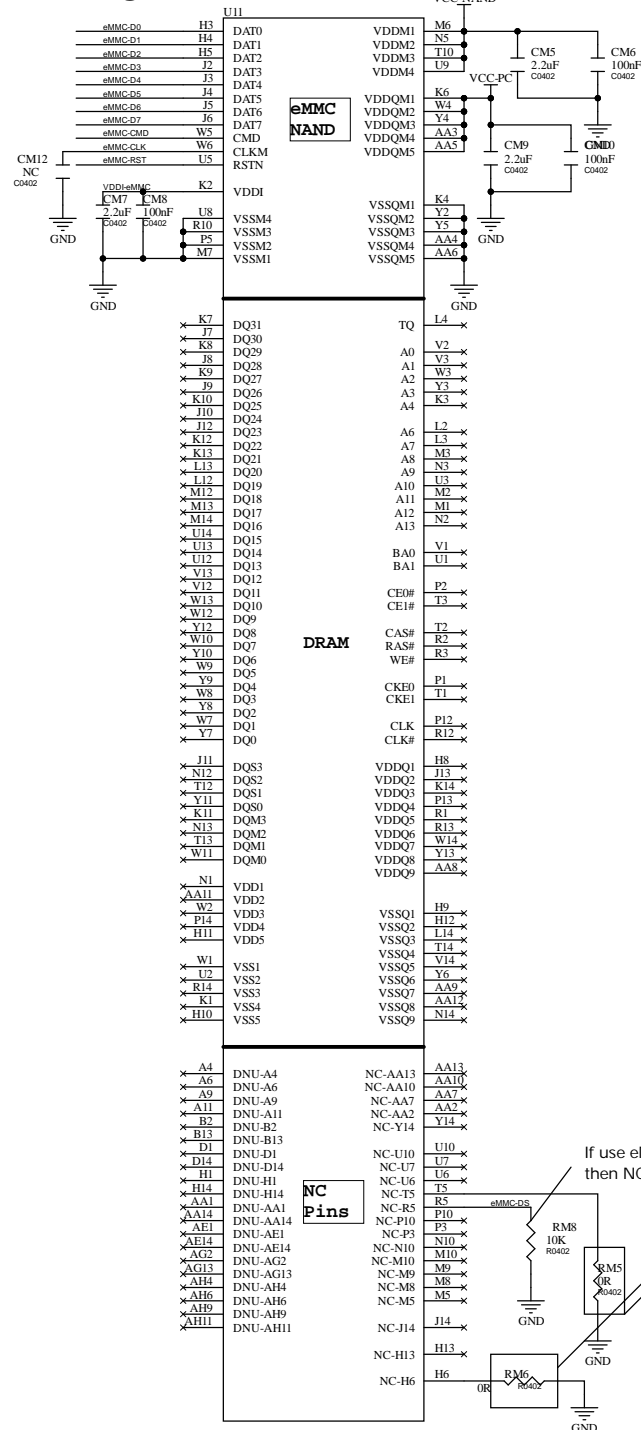
LRDDR4



LPDDR4_FBGA200
bga200p_85X0_8-10X15H_8A
RS512M32LZ4D2ANP



EMMC



If use eMMC is not v5.0/v5.1, then NC this resistor.

If eMMC is not v5.0/v5.1, then NC RM5 and RM6.