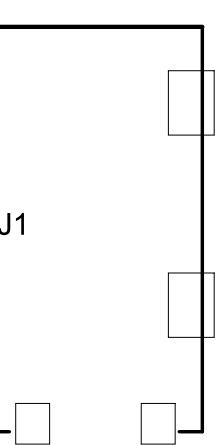


P1

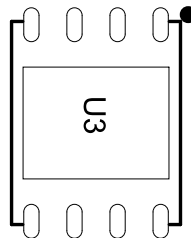


J1

R29
R20
R27
R28

R36
R37

C13
C4



3N

C14
C11
C12
C13

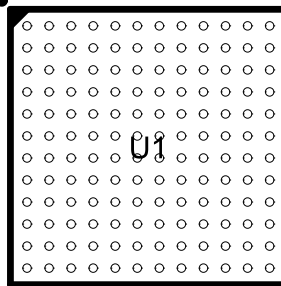
R11
R5

C6
C7
C8

R4
R3

U2

C24
R4
C46
R3

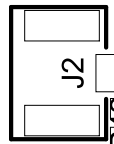


GPIO

C19
C17
C2

C18
C16
C15
C12
C13
C1

C20
C23
C22



J2

R42

ANT A1

R23

R21 R11 R10



L2

C32
C33
C27

U4

R12
R13

R9
R8

L3

L1

C26
C28

C25
C29

C31
C30

L5

U9

C36

U5

P3

C49
R50

R31

R40

C48

R45

C47

C44

BAT

U8

DVP

J4

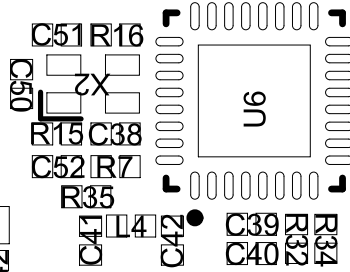
H2.0

R17 R6 C37



P2

20190315 V1.0



DEBUG

C51
R16

R15
C38

C52
R7

R35

C41
C44

C42

R18
R26
R19

U6

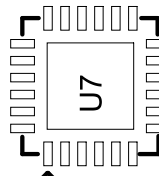
C39
R33
R32

C40
R34

R14

T1
T2

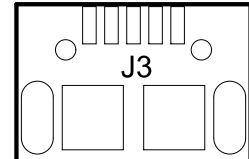
R43



D2
D1

R38
R33
R30
C34
C35

DS3



J3

PWR